Abstracts

Design and Performance of GaAs DCFL Frequency Divider (Short Papers)

R. Kawasaki, M. Ino, M. Hirayama and Y. Takayama. "Design and Performance of GaAs DCFL Frequency Divider (Short Papers)." 1982 Transactions on Microwave Theory and Techniques 30.7 (Jul. 1982 [T-MTT] (Joint Special Issue on GaAs IC's)): 1100-1101.

GaAs frequency dividers for application to gigahertz-band equipment have been developed with E/D DCFL circuits. The relationship between maximum counting frequency and threshold voltage combination of the E-MESFET's and the D-MESFET's has been investigated in detail. The measured performance was as follows maximum counting frequency is 1.1 GHz with low dissipation power of 17 mW, and the values of dissipation power-divided-by-maximum-counting-frequency are as small as 1.6-16 pJ.

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